

in U.S. patent application 09/912,753 filed on July 24, 2001.

Brief Description of the Drawings

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Fig. 1 is a block diagram of a side view of an antenna interface unit.

Fig. 2 is a schematic diagram of a power amplifier matching circuit.

10 Fig. 3 is a schematic diagram of an extended power amplifier matching circuit.

Fig. 4A is a schematic diagram of a multiband power amplifier matching circuit.

15 Fig. 4B is a schematic diagram of another multiband power amplifier matching circuit.

Fig. 5 is a schematic diagram of an isolator and its three matching circuits.

Fig. ^{6A}~~6~~ is a schematic diagram of a LNA matching circuit.

20 Fig. 6B is a graph of a LNA noise figure response.

Fig. 7 is a schematic diagram of an antenna matching circuit.

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